## Amendments to the Claims

## IN THE CLAIMS:

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This listing of claims will replace all prior versions, and listings, of claims regarding the present application. In reading this, text added by the amendment is <u>underlined</u> and canceled text appears in <u>strikethrough</u> or [[double brackets]].

- 1. (Previously presented) A flash memory card interfacing system for detachably coupling
  to a host computer system, the flash memory card interfacing system also configured for
  performing data storage and control operations, the flash memory card interfacing system
  comprising: a device for connecting a flash memory card to a USB port, such that the flash
  memory card operates as a removable data storage for the host computer system.
- 1 2. (Previously presented) A flash memory card interfacing system for coupling a flash memory card to a USB bus, the flash memory card interfacing system comprising:
  - a. a flash memory module for executing a write operation, a read operation, and an erase operation;
  - b. an interface device for coupling the flash memory card to a USB port;
  - c. a USB connector for connecting to the USB bus, sending and receiving signals, and coupling the interface device to the flash memory card;
  - d. a flash controller coupled to the flash memory module and the USB connector, the flash controller controlling the USB connector and configuring the flash memory card to a selected operating mode of the interface device; and
  - e. flash interface circuitry coupled to the flash controller, the flash interface circuitry identifying the selected operating mode of the interface device.
- 1 3. (Original) The flash memory card interfacing system of claim 2, wherein the selected operating mode is a universal serial bus mode.
- 1 4. (Previously presented) The flash memory card interfacing system of claim 2, further comprising a fifty pin connector end configured to couple to the interface device.

- 1 5. (Previously presented) The flash memory card interfacing system of claim 2, further
- 2 comprising a sixty-eight pin connector end configured to couple to the interface device.
- 1 6. (Previously presented) The flash memory card interfacing system of claim 2, wherein the
- 2 flash interface circuitry is mounted on the flash memory card.
- 1 7. (Previously presented) The flash memory card interfacing system of claim 2, wherein the
- 2 flash memory card interfacing system further comprises an adapter module coupled to the flash
- 3 memory card and the interface device.
- 1 8. (Previously presented) The flash memory card interfacing system of claim 7, wherein the
- 2 flash interface circuitry is mounted within the adapter module.
- 9. (Previously presented) A flash memory card interfacing system for detachably coupling
- 2 to a host computer system, the flash memory card interfacing system also configured for
- 3 performing data storage and control operations, the flash memory card interfacing system
- 4 comprising: a device for connecting a flash memory card to a USB port, such that the flash
- 5 memory card automatically configures itself to cooperatively operate in a selected operating
- 6 mode through the device.
- 1 10. (Previously presented) The flash memory card interfacing system of claim 9, wherein the
- 2 selected operating mode is a universal serial bus mode.
- 1 11. (Previously presented) The flash memory card interfacing system of claim 9, further
- 2 comprising a fifty pin connector end configured to couple to the device.
- 1 12. (Previously presented) The flash memory card interfacing system of claim 9, further
- 2 comprising a sixty-eight pin connector end configured to couple to the device.
- 1 13. (Previously presented) The flash memory card interfacing system of claim 9, wherein the
- 2 flash memory card is powered by the host computer system.

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| 1  | 14.  | (Curi    | ently amended) A flash memory card interfacing system for coupling a flash        |
|----|------|----------|---|
| 2  | memo | ory card | I to a USB bus, the flash memory card interfacing system comprising:              |
| 3  |      | a.       | the flash memory card for executing a write operation, a read operation, and an   |
| 4  |      |          | erase operation and comprising: an interface device for coupling the flash memory |
| 5  |      |          | card to a USB port; a flash controller coupled to the flash memory module card    |
| 6  |      |          | and a USB connector, the flash controller controlling the USB connector and       |
| 7  |      |          | configuring the flash memory card to a universal serial bus mode of the interface |
| 8  |      |          | device; and flash interface circuitry coupled to the flash controller, the flash  |
| 9  |      |          | interface circuitry identifying the universal serial bus mode of the interface    |
| 10 |      |          | device; and   |
| 11 |      | b.       | the USB connector for connecting to the USB bus, sending and receiving signals,   |
| 12 |      |          | and coupling the interface device to the flash memory card.                       |

- 1 15. (Previously presented) The flash memory card interfacing system of claim 14, further comprising a fifty pin connector end configured to couple to the interface device.
- 1 16. (Previously presented) The flash memory card interfacing system of claim 14, further comprising a sixty-eight pin connector end configured to couple to the interface device.